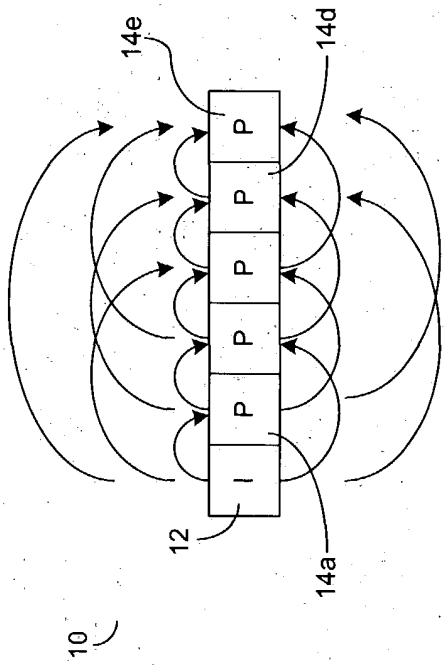

10
12
14a
14d
14e
I
P
P
P
P
P

FIG. 1

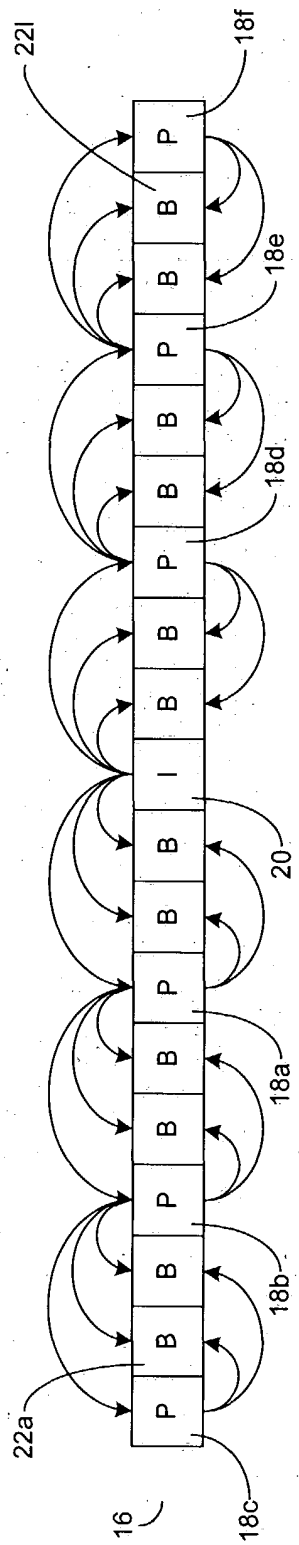

16
18c
18b
18a
18d
18e
18f
20
22a
22l
P
B
B
P
B
B
P
B
B
I
B
B
P
B
B
P
B
B
P

FIG. 2

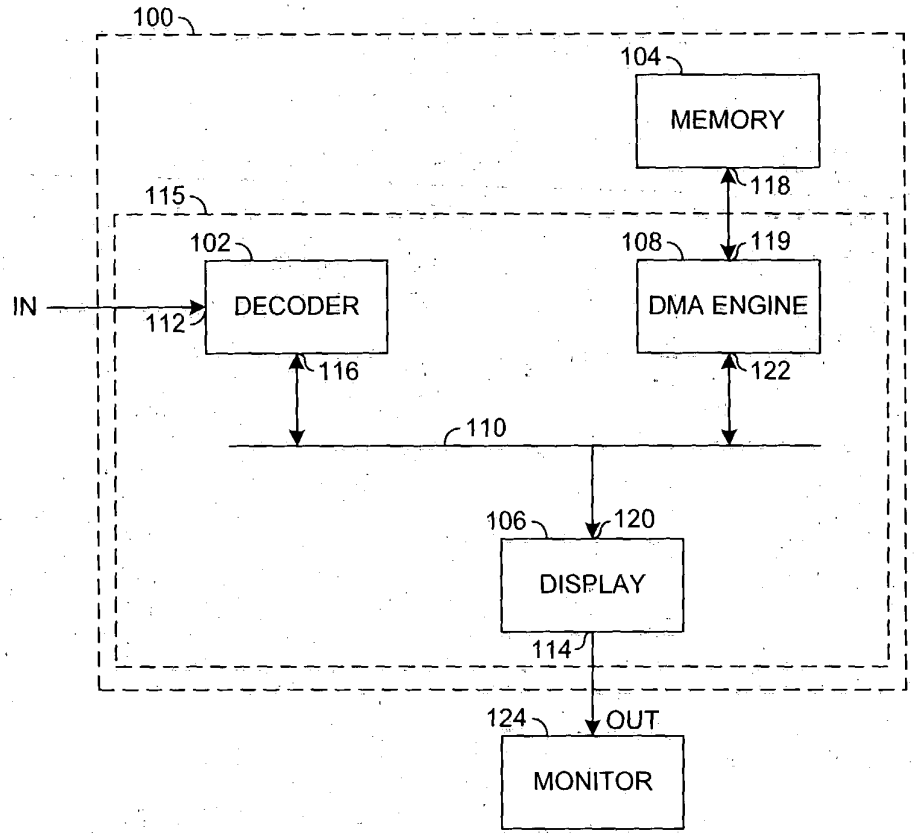
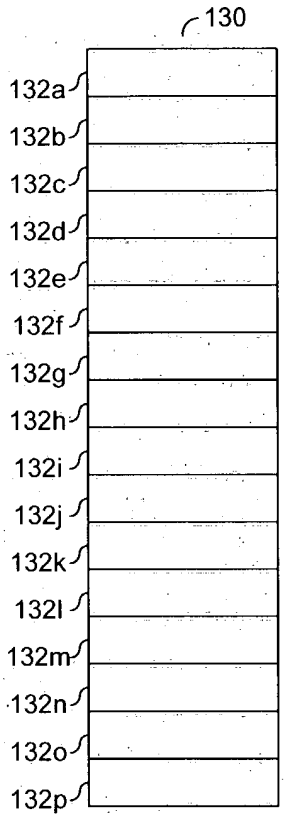
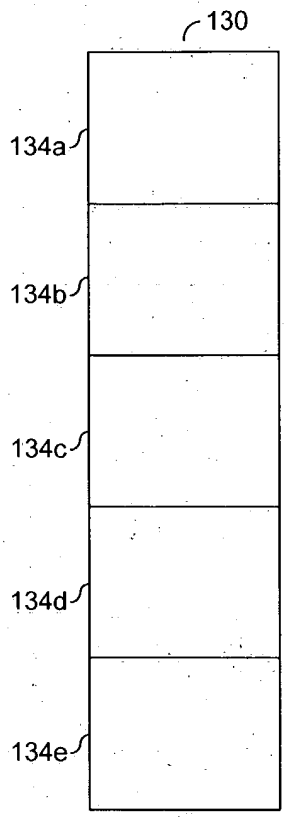

100
104
MEMORY
118
115
102
108
119
IN
DECODER
DMA ENGINE
112
116
122
110
106
120
DISPLAY
114
124
OUT
MONITOR

FIG. 3



DIVIDING FIXED DECODED PICTURE BUFFER INTO 16 SMALL PICTURES

FIG. 4



DIVIDING FIXED DECODED PICTURE BUFFER INTO 5 BIG PICTURES

FIG. 5

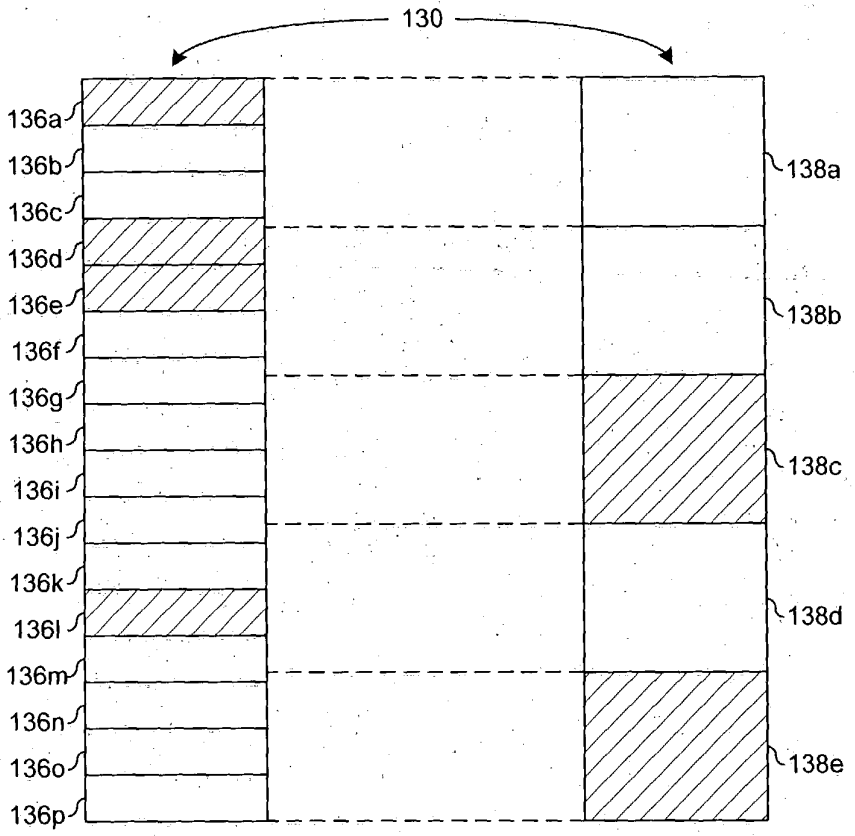

130
136a
136b
136c
136d
136e
136f
136g
136h
136i
136j
136k
136l
136m
136n
136o
136p
138a
138b
138c
138d
138e
DIVIDING FIXED DECODED PICTURE BUFFER INTO 16 SMALL PICTURES. SHADED BUFFERS ARE IN USE.
DIVIDING FIXED DECODED PICTURE BUFFER INTO 5 BIG PICTURES. SHADED BUFFERS ARE AVAILABLE

FIG. 6

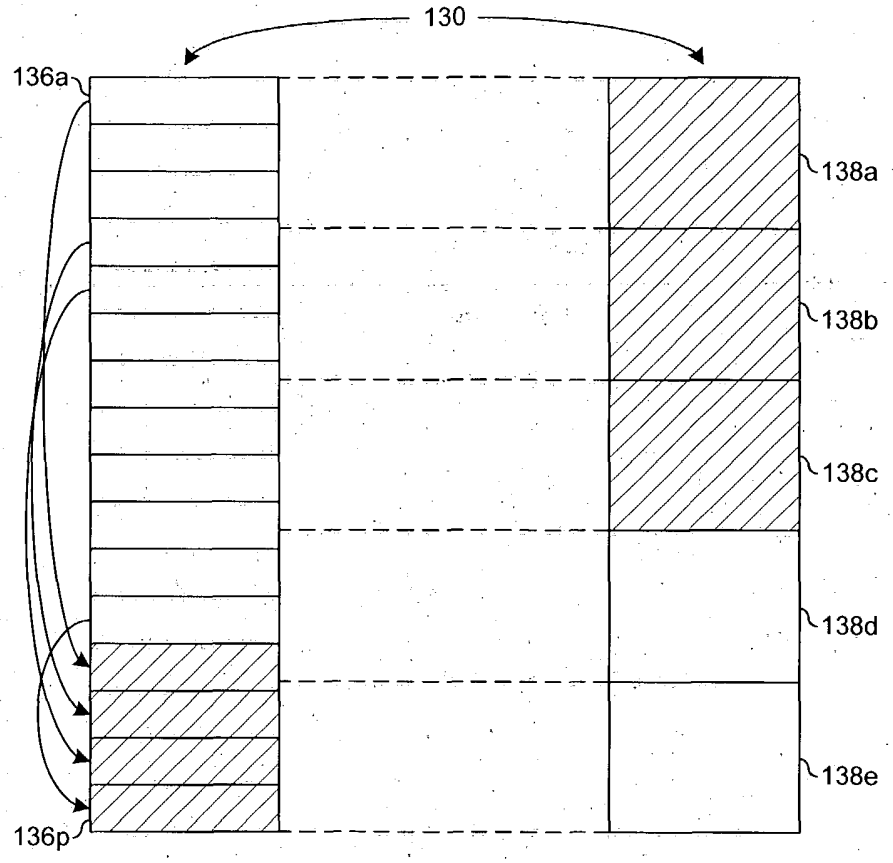

130
136a
136p
138a
138b
138c
138d
138e
DIVIDING FIXED DECODED PICTURE BUFFER INTO 16 SMALL PICTURES. SHADED BUFFERS ARE IN USE.
DIVIDING FIXED DECODED PICTURE BUFFER INTO 5 BIG PICTURES. SHADED BUFFERS ARE AVAILABLE

FIG. 7